

REMARKS

Claims 27-37 and 45-53 are pending in this application. By this Amendment, the title is amended, claims 1, 9-26, 38-44 are canceled, claims 29, 31, 32, and 35-37 are amended, and claims 45-53 are added. Reconsideration based on the above comments and following remarks is respectfully requested.

Applicant appreciates the courtesies shown to Applicant's representative by Examiner Nguyen in the December 13, 2004 personal interview. Applicant's separate record of the substance of the interview is incorporated into the following remarks.

I. The Specification Satisfies All Formal Requirements

The Office Action objects to the specification because the title of the invention is not descriptive. The title is amended to obviate the objection. Withdrawal of this objection is respectfully requested.

II. The Drawings Satisfy 37 C.F.R. §1.83(a)

The Office Action objects to claims 37 and 38 for not showing the feature, "the length of the matrix in a row direction that intersects a column direction along with a plurality of data lines extend being shorter than a length of display section in a row section." Claim 37 is amended to "a length of memory cell section in a row direction . . . " and claim 38 is canceled. The feature of the length of the memory cell section in a row direction that intersects the column direction can be found in the specification at least at paragraph [0077]. Withdrawal of this objection is respectfully requested.

III. The Claims Define Patentable Subject Matter

The Office Action rejects claims 1, 9-15, 27-31, 33, 34 and 36-43 under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 5,815,136 to Ikeda et al. in view of U.S. Patent No. 5,440,718 to Kumagai et al. and further in view of U.S. Patent No. 6,339,417 to Quanrud; claims 16-23, 25, and 26 under 35 U.S.C. §103(a) as being unpatentable over

Ikeda in view of Kumagai and further in view of Quanrud and further in view of U.S. Patent No. 6,025,822 to Motegi et al.; and claims 24, 32, 35 and 44 under 35 U.S.C. §103(a) as being unpatentable over Ikeda in view of Kumagai and further in view of Quanrud and further in view of U.S. Patent No. 5,761,694 to Rao.

With respect to claims 1 and 9-15, and 38-44, these rejections are moot. With respect to the remaining claims, the Final Rejection is respectfully traversed.

With respect to independent claim 27, the Office Action recognizes that Ikeda does not teach the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns and the memory cell section being disposed between the display section and the selection switch and the display section, the memory, the cell section and the selection switch section being formed on one substrate. Applicant respectfully disagrees with the Office Action's assertion that Quanrud provides the deficiencies of Ikeda. Instead, the memory cell disclosed in Quanrud is disposed corresponding to each pixel and acts as a pixel circuit to drive each pixel.

None of the applied references disclose or suggest a length of the memory cell section in a row direction that intersects the column direction along with the plurality of data lines extend, being shorter than a length of the display section in a row direction, as recited in claim 37.

The Office Action recognizes that Ikeda does not specifically teach the plurality of memory cells being arranged in a matrix having a plurality of rows and a plurality of columns, as in original claim 37 and now in new claim 47. Applicants respectfully disagree with the Office Action's assertion that Kumagai provides the deficiencies of Ikeda.

There is no motivation or suggestion to combine the device of Ikeda with the memory device of Kumagai. Ikeda relates to a liquid crystal display. Kumagai, however, does not relate to an electro-optical device such as a display device. Instead, Kumagai merely relates

to a memory device. Thus, there is no motivation to combine the device of Ikeda with the memory device of Kumagai.

Even if there was the necessary motivation or suggestion to combine the references, the combination of liquid crystal display of Ikeda with the memory device of Kumagai, would not disclose or suggest the display device of claim 47. The memory cell section recited in claim 47 avoids the occurrence of useless space on the substrate in an area of the display drive section. See, e.g., at least paragraph [0028]. The combined device would not provide a memory cell section where the plurality of memory cells are arranged in a matrix having a plurality of rows and plurality of columns, and thus, would not solve the problem of reducing the size and power consumption in the driver circuit.

Further, none of the applied references discloses a scanning line driver, as recited in claim 45. Instead, Ikeda discloses a scanning line driver that receives power from the power supply 133 or control signal 2705 (see, *e.g.*, Figures 1B and 27).

For at least the reasons discussed above, claims 27, 37 and 47, and the claims dependent therefrom, are patentable over the applied references.

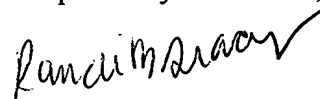
Withdrawal of these rejections is respectfully requested.

IV. Conclusion

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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